

Rate Contract
Government of India
Directorate General of Supplies & Disposals
Jeevan Tara Building, 5 Sansad Marg
New Delhi-110001
Tel Number 23360610/23360537

Rate Contract no. LOGIC_ANA/IT-4/RC-D2070000/0511/84/F0177/1383
Dated 31-MAY-10

To,

AGMATEL INDIA PVT LTD
E-366, IIInd floor, Nirman Vihar,
Delhi-110092, Tel: 011-22462256/22456529
Telefax: 011-22019833
A.K.Aggarwal:9810054742 Email: aka@agmatel.com
Vijay Malhotra: 9810054741 Email: vkm@agmatel.com

Sub: Rate Contract for supply of Logic Analyzer
Validity: From 01-JUN-10 To 31-MAY-11 .

Ref:(1) This Office Tender Enquiry No. LOGIC_ANA/IT-4/RC-D2070000/0511/84
Opened on 19-JAN-10.
(2) Your Quotation No. AIPL/TMI/2009-2010/TLA- RC4 And Dated
12-JAN-10.

Dear Sir,

You are hereby informed that your above referred tender read with subsequent letters mentioned above for the Stores specified in the Schedules annexed has been accepted. This rate contract will be governed by the terms and conditions brought in the Form no. DGS&D 1001 available from dgs&d sales counter on payment of Rs. 50/- .The Rate Contract and the schedules annexed here to shall be the sole repository of this Rate Contract/Transaction.

SCHEDULES ANNEXED

- 1.Schedule "A" Description of stores,prices,duties/taxes.
- 2.Schedule "B" special conditions of contract / Other information.
- 3.Schedule "C" Information to DDOs about parallel rate contracts.
- 4.Annexure - Technical Specification

Yours Faithfully,

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Assistant Director(S)/Section Officer/Dy.Director
For and on behalf of the purchaser named in the Form DGS&D 1001.

COPY TO:

- 1.The Chief Controller of Accounts,Department of commerce,New Delhi/
COA Mumbai/COA Kolkata,COA Chennai.
(Through authentication cell) This issues with the approval of competent authority.
- 2.Deputy Director General of Supplies & Disposals,Chennai -10 copies each
- 3.Deputy Director General of Supplies & Disposals,Kolkata -10 copies each
- 4.Deputy Director General of Supplies & Disposals,Mumbai -10 copies each
- 5.Deputy Director General (QA),Kolkata -10 copies each
- 6.Deputy Director General (QA),Chennai -10 copies each
- 7.Deputy Director General (QA),Mumbai -10 copies each
- 8.Deputy Director General (QA),DGS&D -10 copies each
- 9.Inspection Authority ADG(QA) DGS&D New Delhi - 110001.
- 10.Quality assurance Officer Director (QA) Delhi Directorate/ Mumbai,Chennai/Kolkata.
- 11.Concurrent Audit
- 12.MIS Cell
- 13.Ledger clerk
- 14.O.L.Section
- 15.All Direct Demanding Officers as per mailing list maintained by DGS&D.



Assistant Director (S) / Section Officer / Deputy Director
FOR DIRECTOR GENERAL OF SUPPLIES & DISPOSALS

SCHEDULE - A

1. Rate Contract No.: -LOGIC_ANA/IT-4/RC-D2070000/0511/84/F0177/1383
Dated 31-MAY-10 For the Supply of Logic Analyzer

2. Advance Rate Contract No.: - Nil
Dated

3.(a) Name and Full Address of the Firm :-

AGMATEL INDIA PVT LTD
E-366, IInd floor, Nirman Vihar,
Delhi-110092, Tel: 011-22462256/22456529
Telefax: 011-22019833
A.K. Aggarwal: 9810054742 Email: aka@agmatel.com
Vijay Malhotra: 9810054741 Email: vkm@agmatel.com

DELHI - 110092
Tel. No. - 011-22462256/22456529
Fax - 011-22019833
Email - aka@agmatel.com

(b) Name and Full Address of Manufacturer :-

Tektronix Inc. P.O.Box-500, M/s 50-235
Beaverton, OR 97077 USA

(c) Brand: TEKTRONIX

4. Validity of Rate Contract: 01-JUN-10 To 31-MAY-11

5. Description of Item, Specification, Unit, Rate

| Item Model No. | Store Description | Description | Unit | Rate (in Rs.) |
|----------------|-----------------------------------|-----------------|--|--|
| 1 | Tektronix/ TLA5201B+ P6410(2sets) | Logic Analyzers | Description: Logic Analyzer 34 Channels NOS. | 678568 Rs. SIX LAKHS SEVENTY-EIGHT THOUSAND FIVE HUNDRED SIXTY-EIGHT ONLY |
| 2 | Tektronix/ TLA5202B+ P6410(4sets) | Logic Analyzers | Description: Logic Analyzer 68 Channels NOS. | 894614 Rs. EIGHT LAKHS NINETY-FOUR THOUSAND SIX HUNDRED FOURTEEN ONLY |

| | | | | | |
|---|---|-------------------------------------|--|------|---|
| 3 | Tektronix/ TLA5203B+ P6410(6sets) | Logic Analyzers | Description: Logic Analyzer 102 Channels | NOS. | 1112509 Rs. ELEVEN LAKHS TWELVE THOUSAND FIVE HUNDRED NINE ONLY |
| 4 | Tektronix/ TLA5204B+ P6410(8sets) | Logic Analyzers | Description: Logic Analyzer 136 Channels | NOS. | 1336546 Rs. THIRTEEN LAKHS THIRTY-SIX THOUSAND FIVE HUNDRED FORTY-SIX ONLY |
| 5 | Tektronix - P6434 (1 set) | Add-on Items for Logic Analyzers | Description: Set of High density Mictor Probes for 34 Channels | NOS. | 61700 Rs. SIXTY- ONE THOUSAND SEVEN HUNDRED ONLY |
| 6 | Tektronix - P6434 (2 set) | Add-on Items for Logic Analyzers | Description: Set of High density Mictor Probes for 68 Channels | NOS. | 123400 Rs. ONE LAKH TWENTY- THREE THOUSAND FOUR HUNDRED ONLY |
| 7 | Tektronix - P6434 (3 sets) | Add-on Items for Logic Analyzers | Description: Set of High density Mictor Probes for 102 Channels | NOS. | 199143 Rs. ONE LAKH NINETY- NINE THOUSAND ONE HUNDRED FORTY- THREE ONLY |



| | | | | | |
|----|---------------------------|----------------------------------|---|------|---|
| 8 | Tektronix-P6434 (4 sets) | Add-on Items for Logic Analyzers | Description: Set of High density Mictor Probes for 136 Channels | NOS. | 265429 Rs. TWO LAKHS SIXTY-FIVE THOUSAND FOUR HUNDRED TWENTY-NINE ONLY |
| 10 | Tektronix-TLA5201B (8S) | Add-on Items for Logic Analyzers | Description: Memory upgrade from 512K to 8 Mega Points | NOS. | 153000 Rs. ONE LAKH FIFTY-THREE THOUSAND ONLY |
| 11 | Tektronix-TLA5201B (9S) | Add-on Items for Logic Analyzers | Description: Memory upgrade from 512K to 32 M | NOS. | 756000 Rs. SEVEN LAKHS FIFTY-SIX THOUSAND ONLY |

| | |
|--|--|
| 6-Terms of Delivery: | Free delivery at consignee's end including installation, commissioning and training. |
| 7-Excise Duty: | Not Applicable |
| 8-Sales Tax: | CST/VAT extra @5% or as applicable at the time of Delivery. |
| 9-Delivery Period: | 12-16 weeks from the date of technically and commercially cleared order, road permit and customs duty exemption certificate, wherever applicable. In case of CDEC orders, delivery period will be reckoned from the date of receipt of CDEC. |
| 10(a)-Annual Turnover: | Without Limit |
| (b)-Monetary Limit: | 1000000 |
| 11-Payment Terms: | As per Schedule B |
| 12-Slab Discount Clause: | As per Schedule B |
| 13-Prices: | FIXED |
| 14-Quantity Offered: | Without Limit |
| 15-Minimum Quantity in Single Supply Order: | Without Limit |
| 16-Minimum order Value in Single Supply Order: | Without Limit |
| 17-Status of the RC Holding Firm: | LSI |

- 18-Paying Authority: The Chief Controller of Accounts, Deptt. of Supply
16A, AKBAR ROAD
New Delhi -110011
- 19-Inspection Authority: For Civil
ADG(QA),Jeevan Tara Building,Parliament Street
New Delhi
- 20-Quality Assurance Officer: For Civil
Director(QA), Delhi Directorate,
Jeevan Tara Building, 4th Floor, Sansad Marg
New Delhi
DELHI -110001
- 21-Place where the Stores are to be Tendered
for Inspection: At M/s. Agmatel India Pvt. Ltd. B-28, First Floor, Patparganj
Industrial Area, Delhi- 110092
- 22-R/C is DDOs Operated: Yes

With effect from First October two thousand eight (01-10-2008),all supply order(s) against this Rate Contract must be placed by Direct Demanding Officer(s) on-line through D.G.S&D web site (www.dgsnd.gov.in) (indentor's page) only. Supply order(s) in any other form placed on or after 01-10-2008 shall not be valid and shall not be acted upon by the contractor or any other concerned agencies.

DDO shall download the supply order and send an ink signed copy to the concerned paying authority specified in the rate contract through Registered / speed post immediately after on-line placement of Supply Order(s).

- 23-Packing & Specification : see annexure



Asstt. Director (S) / Section Officer / Dy. Director
For and behalf of the purchaser named in the Form DGS&D 1001.

All the R/C particulars including prices in respect of individual R/Cs are available on DGS&D website which can be accessed by all.The DGS&D website is <http://dgsnd.gov.in>



SCHEDULE - B

RC Specification: 1. All DDO's are advised to place Online Orders on RC holding firm only otherwise DGS&D will not be responsible for any contractual complication including quality & completeness of the material ordered

2. Country of Origin- USA

3. Custom Duty Exemption Discount :

(a) 17.7% - for Defence Customers with Zero percent duty as per custom notification 39/96, against valid customs duty exemption certificate to be issued by order placing Department.

(b) 10% for Education Customers where applicable duty is 9.4% as per custom notification 51/96, against valid customs duty exemption certificate to be issued by order placing Department.

4. PAYMENT TERMS: 98% payment will be made against proof of Inspection & provisional receipt certificate issued by the consignee for the receipt of the material. The balance 2% of the contract price of the stores will be paid on installation, commissioning and final acceptance of stores by the consignee and also submission of bank guarantee for the balance 2% of contract value valid for 2 months beyond the guarantee/warranty period. Indemnifying the purchaser against all losses incurred during the guarantee/warranty period stipulated in the contract.

5. Demonstration of the machines will be carried out free of cost at consignee's place.

6. Machines shall conform to Guarantee/Warranty clause for a period of 12 months on Logic Analyzer from the date of supply / demonstration.

7. Other terms and conditions are as per Form DGS&D-1001.

8. Octroi Duty and Local Taxes:

Normally materials to be supplied to Government Department against Govt. Contracts are exempted from levy of town duty, Octroi duty, terminal tax and other levies of local bodies. The local Town/Municipal Body regulations at times, however, provide for such exemption only on production of such exemption certificate from any authorized officer. Contractors should ensure that stores ordered against contracts placed by this office are exempted from levy of Town duty/Octroi duty, Terminal tax or other local taxes and duties. Wherever, required, they should obtain the exemption certificate from the purchase officer or indenter concerned, to avoid payment of such local taxes or duties. Octroi, entry tax etc on the buyers account in the absence of relevant exemption certificate. Road permit, Waybill to be provided by DDO along with the order.

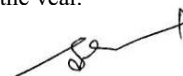
All other terms and conditions as per standard conditions in Form DGS&D-1001.

SCHEDULE -D

SPECIAL INSTRUCTIONS FOR TO R/C HOLDERS

1. Rate Contract holders are advised that before entertaining the supply order(s), they should ensure the availability of following certificates from DDOs.

- a) They are Central Government Department drawing funds from Consolidated Fund of India.
- b) The expenditure involved for the purchase has received the sanction of the competent financial authority.
- c) The funds are available under the proper head in the sanctioned budget allotment for the year.



d) They have been fully authorized by the Department to sign the supply order and incur the liability in respect of the stores being ordered.

2. The rate contract holders should assist the consignee(s) in the installation and operation of the machine, including recommendations for accessories and voltage stabilizer.

(Shashi K. Sud)

Assistant Director (S)

For and on behalf of the purchaser named in the form DGS&D-1001.

Annexure - A

Item No. Description of Store.

MAKE: TEKTRONIX

1. Logic Analyzer, 34 Channel, 2GHz Timing with 125ps MagniVu Acquisition, 235 MHz State, 2Mb Memory with standard accessories

Model: TLA5201B

1a. 17 Channel general purposes Probe

Model No.:P6410 (2Sets)

Total Price: Rs. 6, 78,568/-

2. Logic Analyzer, 68 Channel, 2GHz Timing with 125ps MagniVu Acquisition, 235 MHz State, 2Mb Memory with standard accessories

Model: TLA5202B

2a. 17 Channel general purpose Probe

Model No.:P6410 (4Sets)

Total Price Rs. 8, 94,614/-

3. Logic Analyzer , 102 Channel, 2GHz Timing with 125ps MagniVu Acquisition, 235 MHz State, 2Mb Memory with standard accessories

Model: TLA5203B

3a. 17 Channel general purpose Probe

Model No.:P6410 (6Sets)

Total Price Rs. 11, 12,509/-

4. Logic Analyzer, 136 Channel, 2GHz Timing with 125ps MagniVu Acquisition, 235 MHz State, 2Mb Memory with standard accessories

Model: TLA5204B

4a. 17 Channel general purpose Probe

Model No.:P6410 (8Sets)

Total Price Rs. 13, 36,546/-

5. 34 Channel High Density Mictor Probe

Model P6434 (1 set) Rs. 61,700/-

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6. 34 Channel High Density Mictor Probe
Model P6434 (2 set) Rs. 1,23,400/-

7. 34 Channel high density Mictor probe
Model: P6434(3 Sets)
Total Price Rs. 1, 99,143/-

8. 34 Channel high density Mictor probe
Model: P6434(4 Sets)
Total Price Rs. 2, 65,429/-

10. Memory Upgrade from 512K to 8 MB

1) For 34 Channel Logic Analyzer
Option TLA 5201B-8S
Total Price Rs. 1, 53,000/-

2) For 68 Channel Logic Analyzer
Option TLA 5202B-8S
Total Price Rs. 2, 79,000/-

3) For 102 Channel Logic Analyzer
Option TLA 5203B-8S
Total Price Rs. 3, 96,000/-

4) For 136 Channel Logic Analyzer
Option TLA 5204B-8S
Total Price Rs. 5,22,000/-

11. Memory Upgrade from 512K to 32M

1) For 34 Channel Logic Analyzer
Option TLA 5201B-9S
Total Price Rs. 3, 87,000/-

2) For 68 Channel Logic Analyzer
Option TLA 5202B-9S
Total Price Rs. 5, 62,500/-

3) For 102 Channel Logic Analyzer
Option TLA 5203B-9S
Total Price Rs. 7, 56,000/-

4) For 136 Channel Logic Analyzer
Option TLA 5204B-9S
Total Price Rs. 9, 45,000/-

Note: Memory upgrade option must be ordered along with the main equipment only



(Shashi K. Sud)
Assistant Director (s)
FOR DIRECTORATE GENERAL OF SUPPLIES & DISPOSALS

ANNEXURE

Common requirements for all main equipments:

1. The supply shall include one set of operation and maintenance manual for each equipment.
2. Type test reports for each model shall be from a govt. lab. Type testing shall consist of verification of all the features & functional requirements including environmental tests. The environmental tests sequence will be as under :
Dry Heat Test : For 16 hrs. at a temp. of 55 degree C in accordance with IS:9000/ part-3/section-5/1977 (reaffirmed in 2004).
Cold Test : For 4 hrs. at a temp. of (-)10 degree C in accordance with IS:9000/ part-2/section-4/1977 (reaffirmed in 2004).
Damp Heat Test : For 2 Cycles of 24 hours (12+12h) each at a temp. of 40 degree C & 95 % RH in accordance with IS:9000/part-5/section-2/1981 (reaffirmed in 2004).

The Logic Analyzer shall be checked for all the functional requirements before and after 1-2 hours recovery period of the above environmental tests sequence. There shall be no deterioration in the functional parameters.

OEM Type Test reports with environmental testing to an equivalent International Standard shall also be acceptable in this case.

These reports shall be required to be submitted to the concerned DQA at the time of registration and inspection.

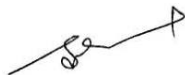
Environmental test mentioned above do not include the requirement of operating condition requirement mentioned for each item in various schedules.

Logic Analyzers

(A) Logic Analyzer 34 channel

Logic Analyzer for measurement and debugging of digital circuit, PC Based

1. Channels : 34
2. Max. State Clock Rate : 235 MHz
3. Timing (all channels, all the time) : To be available
4. Capacitive Loading : < 2 pf
5. Setup/Hold time : Window: 1.5 ns adjustable
6. Simultaneous State and Timing Analysis : To be available
7. Deep Memory Timing : 1 GHz/500 MHz (half/full channels)
8. Memory Depth : 512 K (Full)/1 M(Half) with timestamp, Upgradeable
9. Setup/Hold violation : To be available
10. Trigger States : 16
11. Trigger Types : Word, Range, Glitch with storage, Set-Up/Hold Violation, range, Group, Counter etc.
12. External Peripheral interfaces : LAN, Serial and USB,
13. PC Characteristics : 2GHz or higher Intel Celeron Processor,
512 MB, 80 GB HDD, Windows XP OS, (Open Windows) CD-RW (Internal/External).
14. Display : 10.4 inch/26.5Cms, Active matrix TFT, LCD
15. Probe : 2 sets of 17 Channel General Purpose Passive Probe
16. Operating Temperature : 0 to 45 degree C temperature and 80% RH at 40 degree C.
17. Power Requirement : 230 +/-10% V AC, 50 Hz
18. Interoperability with Oscilloscope : Ability to trigger LA from trigger event of scope and vice versa with time correlation.



(B) Logic Analyzer 68 channel

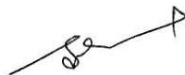
Logic Analyzer for measurement and debugging of digital circuit, PC Based

1. Channels : 68
 2. Max. State Clock Rate : 235 MHz
 3. Timing (all channels, all the time) : To be available
 4. Capacitive Loading : < 2 pf
 5. Setup/Hold time : Window: 1.5 ns adjustable
 6. Simultaneous State and Timing Analysis : To be available
 7. Deep Memory Timing : 1 GHz/500 MHz (half/full channels)
 8. Memory Depth : 512 K (Full)/1 M(Half) with timestamp, Upgradeable
 9. Setup/Hold violation : To be available
 10. Trigger States : 16
 11. Trigger Types : Word, Range, Glitch with storage, Set-Up/Hold Violation, Timer, range, Counter etc.
- Group,
12. External Peripheral interfaces : LAN, Serial and USB,
 13. PC Characteristics : 2 GHz or higher Intel Celeron Processor, 512 MB, 80 GB HDD, Windows XP OS, (Open Windows) CD-
- RW(Internal/External)
14. Display : 10.4 inch/26.5Cms, Active matrix TFT, LCD
 15. Probe : 4 sets of 17 Channel General Purpose Passive Probe
 16. Operating Temperature : 0 to 45 degree C temperature and 80% RH at 40 degree C.
 17. Power Requirement : 230 +/-10% V AC, 50 Hz
 18. Interoperability with Oscilloscope : Ability to trigger LA from trigger event of scope and vice versa with time correlation.

(C) Logic Analyzer 102 channel

Logic Analyzer for measurement and debugging of digital circuit, PC Based

1. Channels : 102
 2. Max. State Clock Rate : 235 MHz
 3. Timing (all channels, all the time) : To be available
 4. Capacitive Loading : < 2 pf
 5. Setup/Hold time : Window: 1.5 ns adjustable
 6. Simultaneous State and Timing Analysis : To be available
 7. Deep Memory Timing : 1 GHz/500 MHz (half/full channels)
 8. Memory Depth : 512 K (Full)/1 M(Half) with timestamp, Upgradeable
 9. Setup/Hold violation : To be available
 10. Trigger States : 16
 11. Trigger Types : Word, Range, Glitch with storage, Set-Up/Hold Violation, Timer, range, Counter etc.
- Group,
12. External Peripheral interfaces : LAN, Serial and USB,
 13. PC Characteristics : 2 GHz or higher Intel Celeron Processor, 512 MB, 80 GB HDD, Windows



XP OS, (Open Windows) CD-

RW(Internal/External).
 14. Display : 10.4 inch/26.5Cms, Active matrix TFT,
 LCD
 15. Probe : 6 sets of 17 Channel General Purpose
 Passive Probe
 16, Operating Temperature : 0 to 45 degree C temperature and 80%
 RH at 40 degree C.
 17. Power Requirement : 230 +/-10% V AC, 50 Hz
 18. Interoperability with Oscilloscope : Ability to trigger LA from trigger
 event of scope and vice versa with
 time correlation.

(D) Logic Analyzer 136 channel

Logic Analyzer for measurement and debugging of digital circuit, PC Based

1. Channels : 136
 2. Max. State Clock Rate : 235 MHz
 3. Timing (all channels, all the time) : To be available
 4. Capacitive Loading : < 2 pf
 5. Setup/Hold time : Window: 1.5 ns adjustable
 6. Simultaneous State and Timing : To be available
 Analysis
 7. Deep Memory Timing : 1 GHz/500 MHz (half/full channels)
 8. Memory Depth : 512 K (Full)/1 M(Half) with timestamp,
 Upgradeable
 9. Setup/Hold violation : To be available
 10. Trigger States : 16
 11. Trigger Types : Word, Range, Glitch with storage, Set-
 Up/Hold Violation, Timer, range,
 Group,
 Counter etc.
 12. External Peripheral interfaces : LAN, Serial and USB,
 13. PC Characteristics : 2 GHz or higher Intel Celeron
 Processor, 512 MB, 80 GB HDD, Windows
 XP OS, (Open Windows) CD-

RW(Internal/External).
 14. Display : 10.4 inch/26.5Cms, Active matrix TFT,
 LCD
 15. Probe : 8 sets of 17 Channel General Purpose
 Passive Probe
 16, Operating Temperature : 0 to 45 degree C temperature and 80%
 RH at 40 degree C.
 17. Power Requirement : 230 +/-10% V AC, 50 Hz
 18. Interoperability with Oscilloscope : Ability to trigger LA from trigger
 event of scope and vice versa with
 time correlation.

Add-on Items for Logic Analyzers

Specification:

Add on items for the above logic Analyzers as per specification mentioned in the
 schedule against each items.

